


[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alt](#)

Welcome United States Patent and Trademark Office

Search Results

[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)

Results for "(((flag*, bit*) <near/12> (sate*, status, condition*) <near/7> register*)<in>m..."

e-mail

Your search matched 7 of 1396453 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.

» Search Options

[View Session History](#)[New Search](#)

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

[Search](#)☐ Check to search only within this results setDisplay Format: ☒ Citation ☐ Citation & Abstract[view selected items](#)[Select All](#) [Deselect All](#)

- ☐ 1. **An architecture for high instruction level parallelism**
Arya, S.; Sachs, H.; Duvvuru, S.;
[System Sciences, 1995. Proceedings of the Twenty-Eighth Hawaii International Conference on](#)
Volume 1, 3-6 Jan. 1995 Page(s):153 - 162 vol.1
Digital Object Identifier 10.1109/HICSS.1995.375398
[AbstractPlus](#) | Full Text: [PDF](#)(924 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ 2. **BLITZEN: a highly integrated massively parallel machine**
Blevins, D.W.; Davis, E.W.; Heaton, R.A.; Reif, J.H.;
[Frontiers of Massively Parallel Computation, 1988. Proceedings. 2nd Symposium on the Frontiers](#)
10-12 Oct. 1988 Page(s):399 - 406
Digital Object Identifier 10.1109/FMPC.1988.47460
[AbstractPlus](#) | Full Text: [PDF](#)(532 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ 3. **Teaching computer architecture in a PC equipped lab using Turbo and Sun assembly langua**
Goda, B.S.; Gray, D.C.; Loy, J.R.;
[Frontiers in Education Conference, 1995. Proceedings, 1995](#)
Volume 1, 1-4 Nov. 1995 Page(s):2a5.14 - 2a5.17 vol.1
Digital Object Identifier 10.1109/FIE.1995.483035
[AbstractPlus](#) | Full Text: [PDF](#)(416 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ 4. **Advanced circuit techniques for high-performance microprocessor and low-power DSPs**
Mathew, S.; Krishnamurthy, R.; Anders, M.; Hsu, S.; Borkar, S.;
[Implementation of High Performance Circuits, 2004. \(DCAS-04\). Proceedings of the 2004 IEEE Da](#)
27 Sept. 2004 Page(s):1 - 45
[AbstractPlus](#) | Full Text: [PDF](#)(5250 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ 5. **Design tradeoffs and experience with Motorola PowerPC migration tools**
Breternitz, M.; Manikonda, A.; Ommerman, M.; Su, W.; Thornton, A.;
[Computer Design: VLSI in Computers and Processors, 1996. ICCD '96. Proceedings, 1996 IEEE I](#)
[Conference on](#)
7-9 Oct. 1996 Page(s):301 - 308
Digital Object Identifier 10.1109/ICCD.1996.563571

[AbstractPlus](#) | Full Text: [PDF](#)(748 KB) | [IEEE CNF](#)
[Rights and Permissions](#)



6. Efficient scan operators for bit-serial processor arrays

Fiduccia, C.M.; Mattheyses, R.M.; Stearns, R.E.;

[Frontiers of Massively Parallel Computation, 1988. Proceedings., 2nd Symposium on the Frontiers](#)

10-12 Oct. 1988 Page(s):105 - 111

Digital Object Identifier 10.1109/FMPC.1988.47420

[AbstractPlus](#) | Full Text: [PDF](#)(560 KB) | [IEEE CNF](#)
[Rights and Permissions](#)



7. Software solutions for the Viterbi algorithm

Ikekawa, M.; Kuroda, I.;

[VLSI Signal Processing, VIII, 1995. IEEE Signal Processing Society \[Workshop on\]](#)

16-18 Sept. 1995 Page(s):90 - 98

Digital Object Identifier 10.1109/VLSISP.1995.527480

[AbstractPlus](#) | Full Text: [PDF](#)(360 KB) | [IEEE CNF](#)
[Rights and Permissions](#)

[Help](#) [Contact Us](#) [Privacy](#)

© Copyright 2006 IEEE

Indexed by
 Inspec®

[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alt](#)

Welcome United States Patent and Trademark Office

Search Results[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)

Results for "(((sate*, status, condition*) <and> register* <and> (card*) <and> (smart, credit..."

☒ e-mail

Your search matched 1 of 1396453 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.

» Search Options

[View Session History](#)[New Search](#)

Modify Search

((((sate*, status, condition*) <and> register* <and> (card*) <and> (smart, credit*))<in>

Search >☐ Check to search only within this results setDisplay Format: ☒ Citation ☐ Citation & Abstract[view selected items](#)[Select All](#) [Deselect All](#)

» Key

IEEE JNL	IEEE Journal or Magazine
IEEE JNL	IEEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEEE CNF	IEEE Conference Proceeding
IEEE STD	IEEE Standard

- ☐ 1. **Closing the loop-payment on demand for interactive services**
Birch, D.G.W.; Shaw, I.J.;
[Broadcasting Convention, 1995, IBC 95, International](#)
14-18 Sep 1995 Page(s):395 - 400
[AbstractPlus](#) | Full Text: [PDF](#)(308 KB) IEEE CNF

Indexed by
[Help](#) [Contact Us](#) [Privacy](#)

© Copyright 2006 IEEE